

DOCKET NO: 241160US2S



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :  
YOSHINORI MATSUBARA : EXAMINER: CLARK, S. V.  
SERIAL NO: 10/632,822 :  
RCE FILED: HEREWITH : GROUP ART UNIT: 2823  
FOR: SEMICONDUCTOR DEVICE, :  
SEMICONDUCTOR DEVICE  
MANUFACTURING METHOD,  
AND SEMICONDUCTOR DEVICE  
TEST METHOD

AMENDMENT FILED CONCURRENTLY WITH  
REQUEST FOR CONTINUED EXAMINATION UNDER 37 C.F.R. § 1.114

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated February 16, 2006, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.